L Number	Hits	Search Text	DB	Time stamp
-	151	(bus with switch\$3) same control\$4 same interfac\$3 same	USPAT;	2004/01/19 10:49
		clock same select\$3	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(dual adj3 clock\$3) same (bus adj3 switch)	USPAT;	2003/12/18 15:37
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	444	data with bus with switch with clock\$1	IBM_TDB USPAT;	2003/12/18 15:38
-	444	data with bus with switch with clocky i	US-PGPUB;	2003/12/10 13.30
			EPO; JPO;	
			DERWENT;	
	ļ		IBM_TDB	
-	414	dual adj clock\$3	USPAT;	2003/12/18 15:42
			US-PGPUB;	
		·	EPO; JPO;	
			DERWENT;	
1			IBM_TDB	00004045 47 15
-	2252	bus adj switch\$1	USPAT;	2003/12/18 15:42
			US-PGPUB;	
			EPO; JPO; DERWENT;	
1			IBM_TDB	
_	0	(dual adj clock\$3) same (bus adj switch\$1)	USPAT;	2003/12/18 15:42
		(dada daj ciconto) camo (dac daj cimonto)	US-PGPUB;	2000/12/10 10/12
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(dual adj clock\$3) and (bus adj switch\$1)	USPAT;	2003/12/18 15:42
			US-PGPUB;	
			EPO; JPO;	
	ļ		DERWENT;	
	4	hus adi (ancad cama switch)	IBM_TDB USPAT;	2003/12/18 15:43
-	*	bus adj (speed same switch)	US-PGPUB;	2003/12/10 13.43
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3543	microprocessor same interface same control same clock\$1	USPAT;	2003/12/19 15:48
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	16	(microprocessor same interface same central same clock#4)	IBM_TDB	2003/12/10 15:52
-	'8	(microprocessor same interface same control same clock\$1) and (bus adj switch)	USPAT; US-PGPUB;	2003/12/19 15:52
		and (buo daj switch)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	927	dual adj3 clock\$3	USPAT;	2003/12/19 15:52
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	11	(dual adj3 clock\$3) same switch same bus	IBM_TDB USPAT;	2003/12/19 16:05
-	''	(dual aujo clockwo) saine switch saine bus	US-PGPUB;	2000/12/19 10:00
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
-	9	kume.in. and nagao.in.	USPAT;	2003/12/19 16:07
1			US-PGPUB;	
1			EPO; JPO;	
			DERWENT;	
L	L		IBM_TDB	

-	643	microprocessor with (semiconductor adj chip)	USPAT; US-PGPUB;	2003/12/22 10:09
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	15	(microprocessor with (semiconductor adj chip)) same	USPAT;	2003/12/22 10:06
		advantage	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	29	microprocessorsame built same (semiconductor adj chip)	USPAT;	2003/12/22 10:09
		same advantage\$1	US-PGPUB;	
	i		EPO; JPO;	
			DERWENT;	
			IBM_TDB	0000404040
-	0	microprocessor same built same (semiconductor adj chip)	USPAT;	2003/12/22 10:09
		same advantage\$1	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	micro-processor same built same (semiconductor adj chip)	USPAT;	2003/12/22 10:46
		same advantage\$1	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	3600	fraguenau adi divisian, adi ratio	IBM_TDB USPAT;	2002/42/22 42:45
-	2690	frequency adj division adj ratio	US-PGPUB;	2003/12/22 12:45
			EPO; JPO;	
1			DERWENT;	
			IBM_TDB	
-	1051	(frequency adj division adj ratio) same clock	USPAT;	2003/12/22 12:46
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1_	71	(frequency adj division adj ratio) same clock same output\$4	IBM_TDB USPAT;	2003/12/22 12:46
1-	''	same (multiple plural plurality)	US-PGPUB;	2003/12/22 12.40
		Carrio (manipio piarar piaranty)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	2211	clock adj control adj circuit	USPAT;	2003/12/22 13:14
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
-	18	(clock adj control adj circuit) same multiple same ratio	USPAT;	2003/12/22 13:14
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	6000	BSC	IBM_TDB USPAT;	2004/01/15 10:14
1	6008		US-PGPUB;	2004/01/15 10:14
			EPO; JPO;	
			DERWENT;	
•			IBM_TDB	
-	286	BSC same clock\$1	USPAT;	2004/01/15 10:15
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
-	15794	((multi multiple plural plurality) adj3 clock\$1)	USPAT;	2004/01/15 11:21
			US-PGPUB;	
			EPO; JPO;	
1			DERWENT;	
	L.,		IBM_TDB	

-	3680	((multi multiple plural plurality) adj3 clock\$1) same select\$3	USPAT; US-PGPUB;	2004/01/15 11:22
			EPO; JPO;	!
			DERWENT;	
	100	(/multi-multiple plural plurality) adi3 clack\$1) same select\$3	IBM_TDB USPAT;	2004/04/15 11:22
-	100	((multi multiple plural plurality) adj3 clock\$1) same select\$3 same ratio	US-PGPUB;	2004/01/15 11:22
		Same ratio	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	236112	clock\$1 same output	USPAT;	2004/01/15 12:54
			US-PGPUB;	
	•		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	14941	clocks with output	USPAT;	2004/01/15 12:55
			US-PGPUB;	!
			EPO; JPO;	
			DERWENT;	
	200	clocks with output with multiple	IBM_TDB USPAT;	2004/01/15 12:55
_	209	clocks with output with multiple	US-PGPUB;	2004/01/15 12:55
			EPO; JPO;	
			DERWENT:	
	ļ		IBM_TDB	
_	1	VL82C315A	USPAT;	2004/01/15 13:24
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	98	SCAMP	USPAT;	2004/01/15 13:24
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
_	39	((slow same fast) adj3 device\$1) same clock\$1	USPAT;	2004/01/15 16:38
		(clott same last) adjo dovisov i samo slosit i	US-PGPUB;	
	ļ		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3042	((slow same fast)) same clock\$1	USPAT;	2004/01/15 16:38
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
_	445	((slow same fast)) same clocks	IBM_TDB USPAT:	2004/01/15 16:38
1	443	(Valor same last) same clocks	US-PGPUB;	2007/01/10 10:00
1	!		EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
-	0	(clock with (selection adj switch)) same (fast with slow)	USPAT;	2004/01/15 17:43
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	40	(clock with (coloct\$2 adi awitch)) some (fact with alaw)	IBM_TDB	2004/01/15 17:40
-	12	(clock with (select\$3 adj switch)) same (fast with slow)	USPAT; US-PGPUB;	2004/01/15 17:43
			EPO; JPO;	
	1		DERWENT;	
			IBM_TDB	
-	26	(CLOCK ADJ OUTPUT) with (selec\$3 adj switch)	USPAT;	2004/01/16 10:41
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	

-	229	different with speed with device\$1 with clock\$1	USPAT;	2004/01/16 10:42
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	23013	(data adj processor).ti.	USPAT;	2004/01/16 14:38
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	334	(clock\$1 same (data adj processor)).ti.	USPAT;	2004/01/16 14:38
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(clock2 same (data adj processor)).ti.	USPAT;	2004/01/16 14:38
			US-PGPUB;	,
į			EPO; JPO;	
			DERWENT;	
1			IBM TDB	
-	14	(clocks same (data adj processor)).ti.	USPAT;	2004/01/16 14:41
			US-PGPUB;	1
			EPO; JPO;	
	[DERWENT;	
			IBM_TDB	
_	38	(two with different with clock\$1) same (fast with slow)	USPAT;	2004/01/16 16:45
		(US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
1_	328	two with separate with clocks	USPAT;	2004/01/18 12:32
	020	The man copulate man disease	US-PGPUB;	200 %0 17 10 12.02
	ļ		EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	21	(fast with slow) same device same clocks same output\$4	USPAT;	2004/01/18 12:42
		(last military) same across same same same	US-PGPUB;	200 110 110 12.12
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	1149	generat\$3 same two same clocks same (different sepatrate)	USPAT;	2004/01/18 13:09
		gonorate came the came came (amorem copation)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	171	generat\$3 same (two adj clocks) same (different sepatrate)	USPAT;	2004/01/18 13:09
	1	(3 (3	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	
-	2238	different with speed with devices	USPAT:	2004/01/18 14:12
			US-PGPUB;	= = = = = = = = = = = = = = = = = = =
			EPO; JPO;	
	1		DERWENT:	
			IBM_TDB	
-	9	different adj speed adj devices	USPAT;	2004/01/18 14:09
	1		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	12548	(different adj speed) clocks with devices	USPAT;	2004/01/18 14:13
	1.20.10	(amount of the start of the sta	US-PGPUB;	
			EPO; JPO;	
	1		DERWENT;	
			IBM_TDB	
L	1	I	<u>,</u>	1

-	0	(different adj speed) with clocks with devices	USPAT;	2004/01/18 14:15
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	suspend\$3 same execut\$3 same instruction\$1 same switch\$3	USPAT;	2004/01/18 15:46
		same clock\$1 same acknowledg\$5 same synchroniz\$5	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/04/40 45 45
-	2	suspend\$3 same execut\$3 same instruction\$1 same switch\$3	USPAT;	2004/01/18 15:47
		same clock\$1 same synchroniz\$5	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	24	suspend\$3 same execut\$3 same instruction\$1 same clock\$1	IBM_TDB USPAT;	2004/01/18 15:47
	24	same synchroniz\$5	US-PGPUB;	2004/01/10 13.47
	·	Same synometry	EPO; JPO;	
			DERWENT;	
			IBM TDB	
	74960	((high fast) same (low slow)) adj3 (device speed)	USPAT;	2004/01/20 15:16
ł	1 4000	(tingin lady dame than didny) adja (davide speca)	US-PGPUB;	2004/01/20 10:10
		,	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	54112	((high fast) same (low slow)) adj (device speed)	USPAT;	2004/01/20 15:16
			US-PGPUB:	
			EPO; JPO;	
	1		DERWENT;	
			IBM_TDB	
-	41845	((high fast) with (low slow)) adj (device speed)	USPAT;	2004/01/20 15:17
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	106	((high fast) with (low slow)) adj speed adj device\$1	USPAT;	2004/01/20 15:17
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	117	pcb same clock same device\$1 same board	USPAT:	2004/01/20 16:07
-	117	pcb same clock same devices i same board	US-PGPUB;	2004/01/20 10.07
1			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
.	1127	((daughter same mother) adj board)	USPAT;	2004/01/20 16:14
		((aaaa)	US-PGPUB:	-30
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	44	((daughter same mother) adj board) same (pc (personal adj	USPAT;	2004/01/20 16:10
		computer))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	}
			IBM_TDB	
-	1	((daughter same mother) adj board) same (clock\$1 adj3	USPAT;	2004/01/20 16:22
		wire\$1)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		//	IBM_TDB	0004/04/05 15 15
-	24	((daughter same mother) adj board) same clock\$1	USPAT;	2004/01/20 16:16
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
L	<u> </u>	I	IBM_TDB	L

-	0	((daughter same mother) adj board) same ((cpu same bus) adj	USPAT;	2004/01/20 16:22
		clock\$1 adj3 wire\$1)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	((daughter same mother) adj board) same ((cpu same bus) adj	USPAT;	2004/01/20 16:22
		clock\$1)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5	(pcb board) same ((cpu same bus) adj clock\$1)	USPAT;	2004/01/20 16:22
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	j
			IBM_TDB	